

Features

- High Density Cell Design for Ultra Low R_{DS(on)}
- · Fully Characterized Avalanche Voltage and Current
- Good Stability and Uniformity with High E_{AS}
- · Epoxy Meets UL 94 V-0 Flammability Rating
- · Moisture Sensitivity Level 1
- Halogen Free Available Upon Request By Adding Suffix "-HF"
- Lead Free Finish/RoHS Compliant ("P" Suffix Designates RoHS Compliant. See Ordering Information)

Maximum Ratings

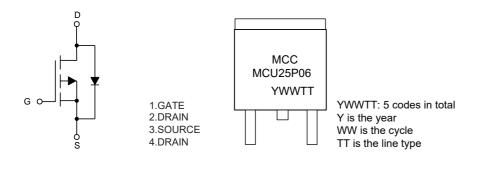
- Operating Junction Temperature Range : -55°C to +150°C
- Storage Temperature Range: -55°C to +150°C
- Thermal Resistance: 1.4°C/W Junction to Case (Note 1)

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V _{DS}	-60	V
Gate-Source Volltage		V _{GS}	±20	V
Continuous Drain Current	T _C =25°C	· I _D	-25	Α
	T _C =100°C		-17.7	Α
Pulsed Drain Current		I _{DM}	-60	Α
Single Pulse Avalanche Energy (Note 2)		E _{AS}	300	mJ
Total Power Dissipation		P _D	90	W

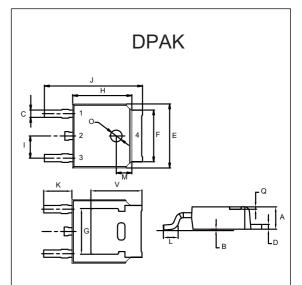
Note: 1.Surface Mounted on FR4 Board, t≤10 sec.

 $2.T_J = 25^{\circ}C, V_{DD} = -20V, V_G = -10V, L = 1mH, R_a = 25\Omega, I_{AS} = 33A.$

Internal Structure and Marking Code



P-CHANNEL MOSFET



DIMENSIONS					
DIM	INC	INCHES		М	NOTE
DIIVI	MIN	MAX	MIN	MAX	NOTE
Α	0.087	0.094	2.20	2.40	
В	0.000	0.005	0.00	0.13	
С	0.026	0.034	0.66	0.86	
D	0.018	0.023	0.46	0.58	
E	0.256	0.264	6.50	6.70	
F	0.201	0.215	5.10	5.46	
G	0.190		4.83		TYP.
Н	0.236	0.244	6.00	6.20	
I	0.086	0.094	2.18	2.39	
J	0.386	0.409	9.80	10.40	
K	0.114		2.90		TYP.
L	0.055	0.067	1.40	1.70	
М	0.063		1.60		TYP.
0	0.043	0.051	1.10	1.30	
Q	0.000	0.012	0.00	0.30	
V	0.211		5.35		TYP.



Electrical Characteristics @ 25°C (Unless Otherwise Specified)

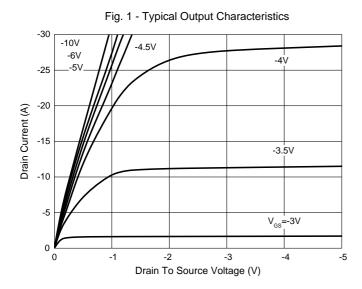
Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Static Characteristics				1	I	
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} =0V, I _D =-250μA	-60			V
Gate-Source Leakage Current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-60V, V _{GS} =0V			-1	μA
Gate-Threshold Voltage ^(Note 3)	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250\mu A$	-2	-2.9	-3.5	V
Drain-Source On-Resistance ^(Note 3)	R _{DS(on)}	V _{GS} =-10V, I _D =-20A		39	45	mΩ
Forward Tranconductance ^(Note 3)	g _{FS}	V _{DS} =-10V, I _D =-10A		25		S
Dynamic Characteristics(Note 4)					•	
Input Capacitance	C _{iss}			3430		pF
Output Capacitance	C _{oss}	V_{DS} =-30V, V_{GS} =0V,f=1MHz		391		
Reverse Transfer Capacitance	C _{rss}			272		
Total Gate Charge	Qg			46		nC
Gate-Source Charge	Q _{gs}	V _{DS} =-30V,V _{GS} =-10V,I _D =-20A		9.5		
Gate-Drain Charge	Q_{gd}			10.5		
Turn-On Delay Time	t _{d(on)}			12		
Turn-On Rise Time	t _r	V_{DD} =-30V, R _L =1.5 Ω ,		15		ns
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10V, R_{G} =3 Ω		38		
Turn-Off Fall Time	t _f			15		
Drain-Source Body Diode Cha	racteristi	cs		1	I	
Continuous Body Diode Current	Is	T _C =25°C			-25	Α
Body Diode Voltage	V _{SD}	I _{SD} =-10A, V _{GS} =0V			-1.2	V
Reverse Recovery Time	t _{rr}	T _{.l} =25°C, I _E =-10A,di/dt=-100A/µs		47		ns
Reverse Recovery Charge	Q _{rr}	1 _J -20 O, 1 _F 10A,αI/αι100A/μS		53		nC
Forward Turn-On Time	t _{on}	Intrinsic Turn-On Time is Negligible (Turn-On is Dominated by LS+LD)				

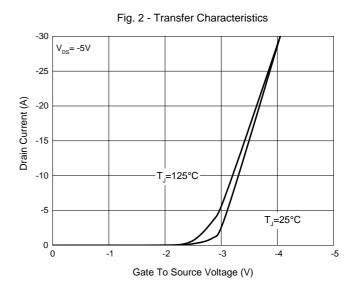
Note 3. Pulse Test : Pulse Width≤300µs, Duty Cycle ≤2%.

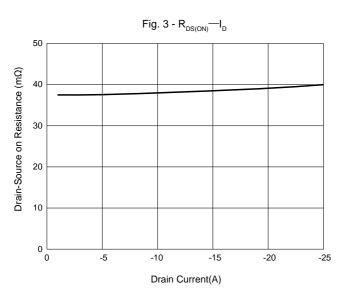
4. Guaranteed by Design, Not Subject to Production Testing.

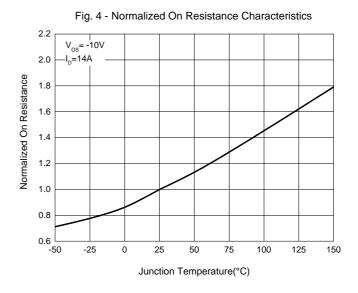


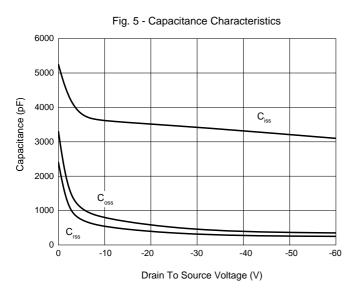
Curve Characteristics

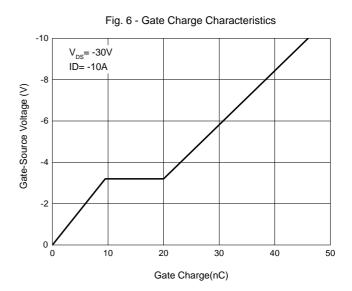














Ordering Information

Device	Packing
Part Number-TP	Tape&Reel: 2.5Kpcs/Reel

Note: Adding "-HF" Suffix for Halogen Free, eg. Part Number-TP-HF

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