



Package Bill of Material Gold (Au) To Copper (Cu) Wire Change For Spartan-3A DSP FPGA Products

XCN14001 (v1.0) December 9, 2013

Product Change Notice

Overview

The purpose of this notification is to announce the transitioning of all wire bond package types for Spartan®-3A DSP FPGA Products from gold (Au) to copper (Cu) wire. Automotive “XA” and Hi-Rel “XQ” devices are not affected. This change will not affect fit, form, function or MSL rating of the packages.

Description

Xilinx will be transitioning all wire bond package types for Spartan®-3A DSP FPGA Products from gold (Au) to copper (Cu) wire. For the Cu-wire assembly, only halogen free, EU-ROHS compliant packages and green mold compound will be used. These packages do not contain published REACH SvHC materials.

Xilinx is converting to copper wire in order to align with the current industry trend to better support long-term demand for the affected products. Copper wire has demonstrated better electrical and mechanical performance than gold wire. Xilinx assembly suppliers have qualified copper wire technology and have been using it in high volume production since 2008. Xilinx has successfully implemented Spartan®-3/-3E/-3A with copper wire since August 2011 (please refer to [XCN11002](#)).

This change will not affect fit, form, function or MSL rating of the packages. Xilinx will continue to support packages assembled with tin lead (SnPb) solder balls and leadframe plating. Any planned capacity expansion will be for copper wire packages only.

Products Affected

This change affects all speed, package, and temperature variations of “XC” commercial (C) and industrial (I) grade product families mentioned in the title and overview. Automotive “XA” and Hi-Rel “XQ” are not affected.

Affected part numbers are included in the [Table 1](#):

Table 1: Spartan-3A DSP FPGA Products Affected

Part Number	Part Number	Part Number
XC3SD1800A-4CS484C	XC3SD1800A-4FGG676I0100	XC3SD3400A-4CSG484I184
XC3SD1800A-4CS484I	XC3SD1800A-5CS484C	XC3SD3400A-4CSG484LI
XC3SD1800A-4CS484LI	XC3SD1800A-5CSG484C	XC3SD3400A-4FG676C
XC3SD1800A-4CSG484C	XC3SD1800A-5FG676C	XC3SD3400A-4FG676I
XC3SD1800A-4CSG484I	XC3SD1800A-5FGG676C	XC3SD3400A-4FGG676C
XC3SD1800A-4CSG484LI	XC3SD3400A-4CS484C	XC3SD3400A-4FGG676I
XC3SD1800A-4FG676C	XC3SD3400A-4CS484I	XC3SD3400A-5CS484C
XC3SD1800A-4FG676I	XC3SD3400A-4CS484LI	XC3SD3400A-5CSG484C
XC3SD1800A-4FGG676C	XC3SD3400A-4CSG484C	XC3SD3400A-5FG676C
XC3SD1800A-4FGG676I	XC3SD3400A-4CSG484I	XC3SD3400A-5FGG676C

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Key Dates and Ordering Information

Xilinx will begin to cross-ship products using both gold wire and copper wire per the [Table 2](#) schedule.

Table 2: Spartan-3A DSP FPGA Products Cross-Ship Schedule

Devices	Packages	Target Cross Shipping Date
XC3SD1800A XC3SD3400A	CS(G)484	April 1 st , 2014
XC3SD1800A XC3SD3400A	FG(G)676	April 1 st , 2014

Qualification Data

Xilinx has successfully passed and completed the Cu wire qualification. Please refer to Xilinx [RPT156](#) for more detail.

Response

No response is required. For additional information or questions, please contact [Xilinx Technical Support](#).

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Additional Documentation

Qualification Report (RPT156):
<https://secure.xilinx.com/webreg/clickthrough.do?cid=173884>

Revision History

The following table shows the revision history for this document.

Date	Version	Revision
12/09/2013	1.0	Initial release.

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