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### Vishay Semiconductors

# Hyperfast Rectifier, 8 A FRED Pt®



PRIMARY CHARACTERISTICS								
I <sub>F(AV)</sub>	8 A							
V <sub>R</sub>	300 V							
V <sub>F</sub> at I <sub>F</sub>	0.83 V							
t <sub>rr</sub> typ.	See Recovery table							
T <sub>J</sub> max.	175 °C							
Package	TO-220AC 2L							
Circuit configuration	Single							

#### **FEATURES**

- Hyperfast recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Designed and gualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

#### **DESCRIPTION / APPLICATIONS**

300 V series are the state of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, DC/DC converters as well as freewheeling diodes in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS					
Repetitive peak reverse voltage	$V_{RRM}$		300	V					
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 155 °C	8	۸					
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>C</sub> = 25 °C	100	A					
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C					

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	300	-	-					
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 8 A	-	1.0	1.25	V				
		I <sub>F</sub> = 8 A, T <sub>J</sub> = 125 °C	-	0.83	1.00					
Deverse legicade everent		$V_R = V_R$ rated	-	0.02	20					
Reverse leakage current	I <sub>R</sub>	$T_J = 125$ °C, $V_R = V_R$ rated	-	6.0	200	μA				
Junction capacitance	C <sub>T</sub>	$V_{R} = 300 \text{ V}$	-	31	-	pF				
Series inductance L <sub>S</sub> Me		Measured lead to lead 5 mm from package body	-	8	-	nH				



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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>C</sub> = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS			
Reverse recovery time	t <sub>rr</sub>	$I_F = 1 A, dI_F/dt = -50$	$I_F = 1 \text{ A, } dI_F/dt = -50 \text{ A/}\mu\text{s, } V_R = 30 \text{ V}$			35				
		T <sub>J</sub> = 25 °C		-	27	-	ns A			
		T <sub>J</sub> = 125 °C	I <sub>F</sub> = 8 A	-	40	-				
Dools woods on a summent	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C		=	2.2	-				
Peak recovery current		T <sub>J</sub> = 125 °C	$dI_F/dt = -200 A/\mu s$ $V_R = 200 V$	=	5.3	-				
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C	''	-	30	-	nC			
		T <sub>J</sub> = 125 °C		-	106	-				

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C				
Thermal resistance, junction to case per leg	R <sub>thJC</sub>		-	1.45	2.5					
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>	Typical socket mount		-	70	°C/W				
Thermal resistance, case to heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	0.2	-					
Weight			-	2.0	-	g				
vveigiti			-	0.07	-	oz.				
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)				
Marking device		Case style TO-220AC 2L	8ETH03							

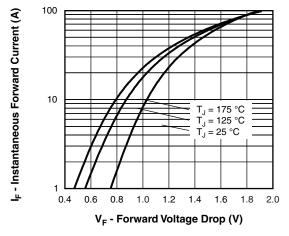


Fig. 1 - Typical Forward Voltage Drop Characteristics

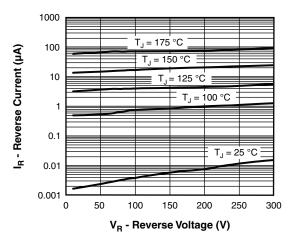


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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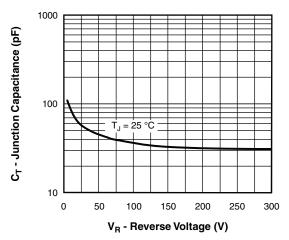


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

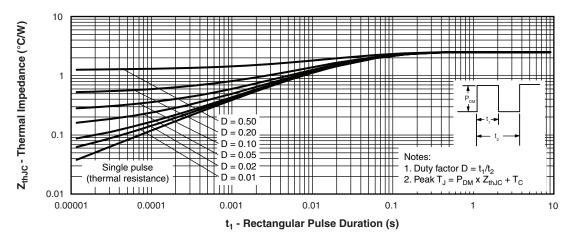


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

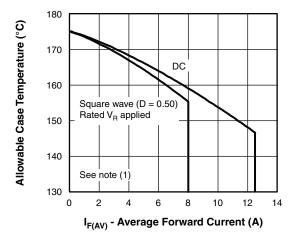
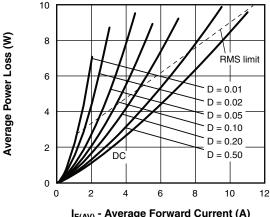


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current



I<sub>F(AV)</sub> - Average Forward Current (A)
Fig. 6 - Forward Power Loss Characteristics

#### Note

 $^{(1)}$  Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC};$   $Pd = forward power loss = I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6);  $Pd_{REV} = inverse power loss = V_{R1} \times I_R$  (1 - D);  $I_R$  at  $V_{R1} = rated \ V_R$ 

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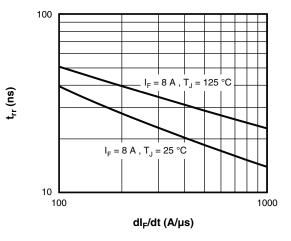


Fig. 7 - Typical Reverse Recovery Time vs. dl<sub>F</sub>/dt

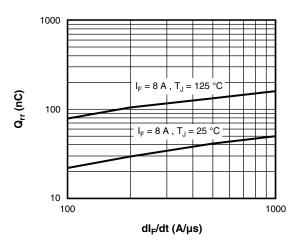
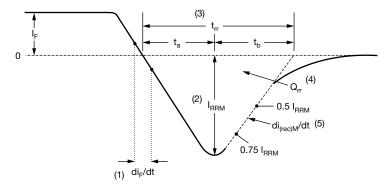


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt



- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $\ensuremath{\text{I}_{\text{RRM}}}$  peak reverse recovery current
- (3)  $t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $I_{\rm F}$  to point where a line passing through 0.75  $I_{\rm RRM}$  and 0.50  $I_{\rm RRM}$ extrapolated to zero current.
- (4)  $Q_{rr}$  area under curve defined by  $t_{rr}$ and I<sub>RRM</sub>

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5)  $di_{(rec)M}/dt$  - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

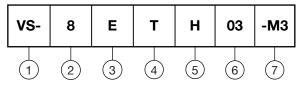
Fig. 1 - Reverse Recovery Waveform and Definitions



### Vishay Semiconductors

### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay Semiconductors product

2 - Current rating (8 = 8 A)

**3** - E = single

Package:

T = 2L TO-220AC

H = hyperfast recovery

Voltage rating (03 = 300 V)

7 - Environmental digit:

-M3 = halogen-free, RoHS-compliant, and termination lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N	BASE QUANTITY	PACKAGING DESCRIPTION							
VS-8ETH03-M3	50	Antistatic plastic tubes							

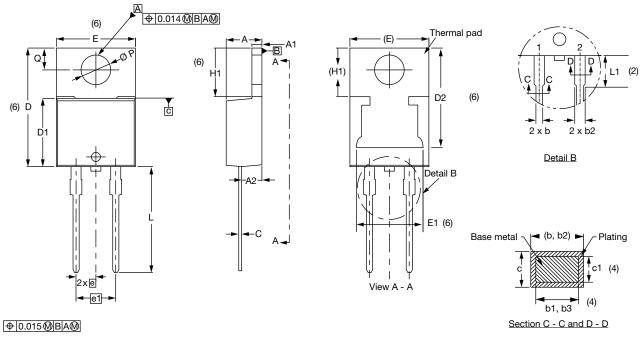
LINKS TO RELATED DOCUMENTS								
Dimensions <u>www.vishay.com/doc?96156</u>								
Part marking information	www.vishay.com/doc?95391							



## Vishay Semiconductors

### **TO-220AC 2L**

#### **DIMENSIONS** in millimeters and inches



Lead tip

Conforms to JEDEC® outline TO-220AC

SYMBOL	MILLIN	IETERS	INC	HES	NOTES		SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	NOTES	STIVIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.25	4.65	0.167	0.183			D2	11.68	13.30	0.460	0.524	6, 7
A1	1.14	1.40	0.045	0.055			E	10.11	10.51	0.398	0.414	3, 6
A2	2.50	2.92	0.098	0.115			E1	6.86	8.89	0.270	0.350	6
b	0.69	1.01	0.027	0.040			е	2.41	2.67	0.095	0.105	
b1	0.38	0.97	0.015	0.038	4		e1	4.88	5.28	0.192	0.208	
b2	1.20	1.73	0.047	0.068			H1	6.09	6.48	0.240	0.255	6
b3	1.14	1.73	0.045	0.068	4		L	13.52	14.02	0.532	0.552	
С	0.36	0.61	0.014	0.024			L1	3.32	3.82	0.131	0.150	2
c1	0.36	0.56	0.014	0.022	4		ØΡ	3.54	3.91	0.139	0.154	
D	14.85	15.35	0.585	0.604	3		Q	2.60	3.00	0.102	0.118	
D1	8.38	9.02	0.330	0.355								

#### Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Lead dimension and finish uncontrolled in L1
- (3) Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Dimension b1, b3, and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2, and E1
- (7) Outline conforms to JEDEC® TO-220, except D2



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