



100V 175°C N-CHANNEL ENHANCEMENT MODE MOSFET PowerDI5060-8

Product Summary

BV _{DSS}	R _{DS(ON)} Max	I _D T _C = +25°C
400)/	14.5mΩ @ V _{GS} = 10V	56A
100V	19.5mΩ @ V _{GS} = 6V	49A

Description and Applications

This new generation N-Channel Enhancement Mode MOSFET is designed to minimize R_{DS(ON)}, yet maintain superior switching performance. This device is ideal for use in:

- Motor control
- DC-DC converters
- Power management

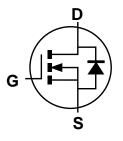
Features

- Rated to +175°C—Ideal for High Ambient Temperature
- 100% Unclamped Inductive Switching (UIS) Test in Production— Ensures More Reliable and Robust End Application
- Thermally Efficient Package—Cooler Running Applications
- Low R_{DS(ON)}—Minimizes On-State Losses
- Low Input Capacitance
- Fast Switching Speed
- Lead-Free Finish; RoHS Compliant (Notes 1 & 2)
- Halogen- and Antimony-Free. "Green" Device (Note 3)
- This part is qualified to JEDEC standards (as references in AEC-Q) for High Reliability.
 - https://www.diodes.com/quality/product-definitions/
- An automotive-compliant part is available under separate datasheet (DMTH10H015SPSQ)

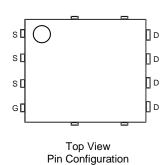
Mechanical Data

- Package: PowerDI[®]5060-8
- Package Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminal Connections Indicator: See Diagram
- Terminals: Finish Matte Tin Annealed over Copper Lead-Frame. Solderable per MIL-STD-202, Method 208
- Weight: 0.097 grams (Approximate)





Internal Schematic



Site 2:

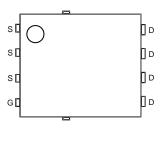
PowerDI5060-8 (SWP) (Type UX)





Bottom View

S



Internal Schematic

Top View Pin Configuration

Top View



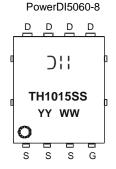
Ordering Information (Note 4)

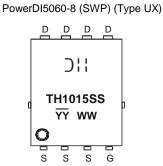
Orderable Part Number	Package	Packing		
Orderable Fait Number	Fackage	Quantity	Carrier	
DMTH10H015SPS-13	PowerDI5060-8	2,500	Tape & Reel	
DMTH10H015SPS-13	PowerDI5060-8 (SWP) (Type UX)	2,500	Tape & Reel	

Notes:

- 1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
- 2. See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

Marking Information





DIII = Manufacturer's Marking
TH1015SS = Product Type Marking Code
YYWW or YYWW = Date Code Marking
YY or YY = Last Two Digits of Year (ex: 23 = 2023)
WW = Week Code (01 to 53)

Maximum Ratings (@ T_A = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			V_{DSS}	100	V
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 5) $V_{GS} = 10V$ Steady $T_C = +25^{\circ}C$ State $T_C = +100^{\circ}C$			I _D	56 39	А
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)	I _{DM}	120	Α		
Maximum Continuous Body Diode Forward Current (Note 5)			Is	56	Α
Avalanche Current L = 3mH			I _{AS}	7.5	Α
Avalanche Energy L = 3mH			Eas	85	mJ
Avalanche Current, L = 0.1mH			I _{AS}	15.8	Α
Avalanche Energy, L = 0.1mH			Eas	12.5	mJ

Thermal Characteristics

Characteristic	Symbol	Value	Unit	
Total Power Dissipation (Note 6)	$T_A = +25^{\circ}C$	P_{D}	2.7	W
Thermal Resistance, Junction to Ambient (Note 6)		R _{ÐJA}	55	°C/W
Total Power Dissipation (Note 5)	T _C = +25°C	P _D	94	W
Thermal Resistance, Junction to Case (Note 5)		Rejc	1.6	°C/W
Operating and Storage Temperature Range		$T_{J_i}T_{STG}$	-55 to +175	°C

Notes: 5. Thermal resistance from junction to soldering point (on the exposed drain pad).

6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate.



Electrical Characteristics (@ T_A = +25°C, unless otherwise specified.)

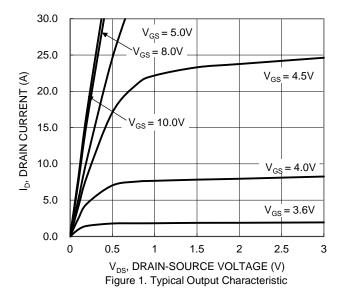
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV _{DSS}	100	_	_	V	$V_{GS} = 0V$, $I_D = 1mA$	
Zero Gate Voltage Drain Current	I _{DSS}	_	_	1	μA	$V_{DS} = 80V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	_	_	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	V _{GS(TH)}	2	_	4	V	$V_{DS} = V_{GS}$, $I_D = 250\mu A$	
Static Drain-Source On-Resistance		_	11.3	14.5	mΩ	$V_{GS} = 10V, I_D = 20A$	
Static Dialit-Source Off-Resistance	R _{DS(ON)}	_	14.7	19.5	11152	$V_{GS} = 6V, I_D = 20A$	
Diode Forward Voltage	V_{SD}	_	0.9	1.3	V	$V_{GS} = 0V, I_{S} = 20A$	
DYNAMIC CHARACTERISTICS (Note 8)							
Input Capacitance	Ciss	_	2343	_	pF	V _{DS} = 50V, V _{GS} = 0V f = 1MHz	
Output Capacitance	Coss	_	487	_			
Reverse Transfer Capacitance	C _{rss}	_	26	_			
Gate Resistance	R _G	_	0.69	_	Ω	$V_{DS} = 0V$, $V_{GS} = 0V$, $f = 1MHz$	
Total Gate Charge	Q_G	_	30.1	_		V 50V L 40A	
Gate-Source Charge	Q_{GS}	_	7.5	_	nC	$V_{DD} = 50V, I_D = 10A,$ $V_{GS} = 10V$	
Gate-Drain Charge	Q_{GD}	_	6.5	_			
Turn-On Delay Time	t _{D(ON)}	_	9.8	_		$V_{DD} = 50V, V_{GS} = 10V,$ $I_{D} = 10A, R_{G} = 6\Omega$	
Turn-On Rise Time	t _R	_	7.8	_			
Turn-Off Delay Time	t _{D(OFF)}		22.5	_	ns		
Turn-Off Fall Time	t _F	_	9.6	_			
Reverse Recovery Time	t _{RR}		43.1	_	ns	I_ 10.0 di/dt 100.0/v.o	
Reverse Recovery Charge	Q _{RR}	_	65.1	_	nC	I _F = 10A, di/dt = 100A/μs	

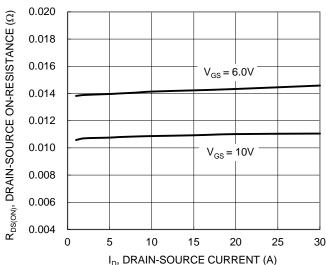
Notes:

^{7.} Short duration pulse test used to minimize self-heating effect. 8. Guaranteed by design. Not subject to product testing.









I_D, DRAIN-SOURCE CURRENT (A)
Figure 3. Typical On-Resistance vs. Drain Current and
Gate Voltage

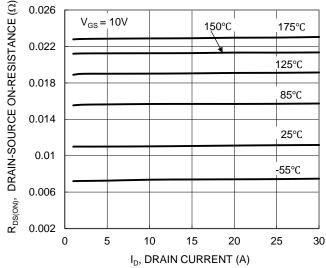
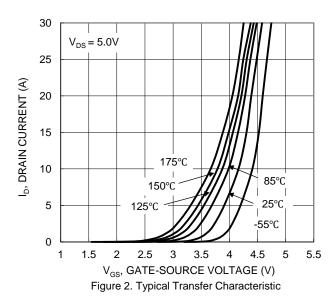
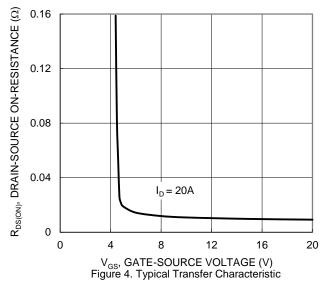


Figure 5. Typical On-Resistance vs. Drain Current and Temperature





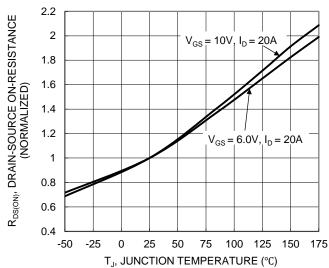


Figure 6. On-Resistance Variation with Temperature

125

150

100

 $I_D = 1 \text{mA}$

 $I_{D} = 250 \mu A$

25

50

Temperature

 C_{iss}

 $\mathsf{C}_{\mathsf{oss}}$

 C_{rss}

30

V_{DS}, DRAIN-SOURCE VOLTAGE (V) Figure 10. Typical Junction Capacitance

40

50

60

75



30

25

20

15

10

5

0

0

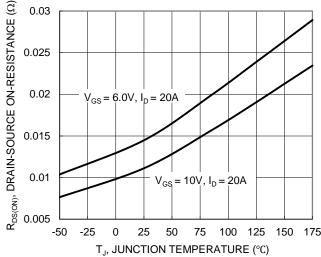
Is, SOURCE CURRENT (A)

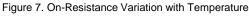
 $V_{GS} = 0V$

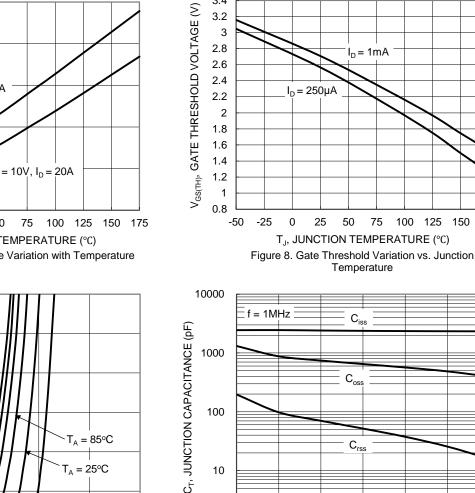
T_A = 175°C

150°C

0.3







1.5

3.4

3.2 3

2.8

2.6 2.4

2.2 2

1.8 1.6 1.4

> 1 0

10

20

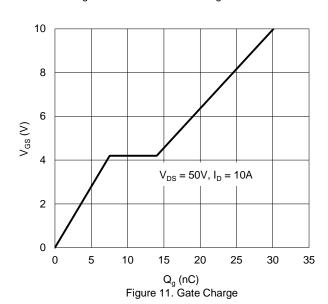
 V_{SD} , SOURCE-DRAIN VOLTAGE (V) Figure 9. Diode Forward Voltage vs. Current

0.9

0.6

 $T_A = -55^{\circ}C$

1.2



1000 R_{DS(ON)} Limited 100 ID, DRAIN CURRENT (A) 10 1 = 10ms $P_W = 100 ms$ $T_{J(Max)} = 175^{\circ}C$ $T_C = 25^{\circ}C$ Single Pulse $P_W = 1s$ DUT on 1*MRP Board $V_{GS} = 10V$ 0.01 0.1 10 100 1000 V_{DS}, DRAIN-SOURCE VOLTAGE (V)

Figure 12. SOA, Safe Operation Area







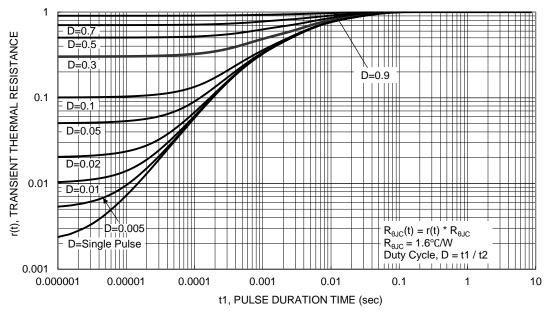


Figure 13. Transient Thermal Resistance

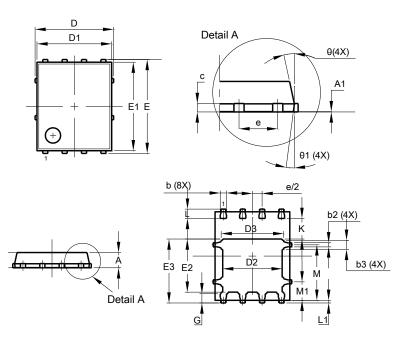


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

Site 1:

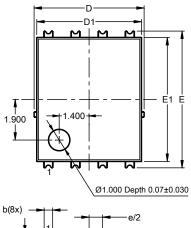
PowerDI5060-8

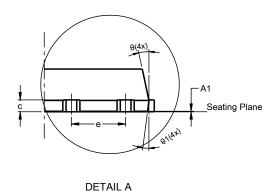


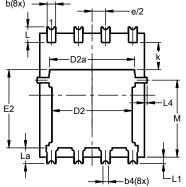
PowerDI5060-8				
Dim	Min	Max	Тур	
Α	0.90	1.10	1.00	
A1	0.00	0.05	-	
b	0.33	0.51	0.41	
b2	0.200	0.350	0.273	
b3	0.40	0.80	0.60	
C	0.230	0.330	0.277	
D		5.15 BSC		
D1	4.70	5.10	4.90	
D2	3.70	4.10	3.90	
D3	3.90	4.30	4.10	
Е		6.15 BSC		
E1	5.60	6.00	5.80	
E2	3.28	3.68	3.48	
E3	3.99	4.39	4.19	
е	,	1.27 BSC		
G	0.51	0.71	0.61	
K	0.51	-	-	
L	0.51	0.71	0.61	
L1	0.100	0.200	0.175	
M	3.235	4.035	3.635	
M1	1.00	1.40	1.21	
Θ	10°	12°	11°	
Θ1	6°	8°	7°	
All Dimensions in mm				

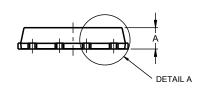
Site 2:

PowerDI5060-8 (SWP) (Type UX)









PowerDI5060-8 (SWP) (Type UX)				
Dim	Min	Max	Тур	
Α	0.90	1.10	1.00	
A1	0	0.05		
b	0.30	0.50	0.41	
b2	0.20	0.35	0.25	
b4	C).25REF	=	
С	0.230	0.330	0.277	
D		.15 BS0	2	
D1	4.70	5.10	4.90	
D2	3.56	3.96	3.76	
D2a	3.78	4.18	3.98	
Е	6	.40 BS0	3	
E1	5.60	6.00	5.80	
E2	3.46	3.86	3.66	
E2a	4.195	4.595	4.395	
е	1	.27BSC		
k	1.05	-		
L	0.635	0.835	0.735	
La	0.635	0.835	0.735	
L1	0.200	0.400	0.300	
L1a	0.050REF			
L4	0.025	0.225	0.125	
М	3.205	4.005	3.605	
θ	10°	12°	11°	
θ1	6°	8°	7°	
All	All Dimensions in mm			

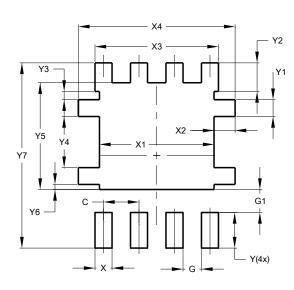


Suggested Pad Layout

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Site 1:

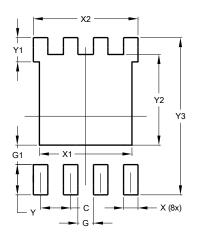
PowerDI5060-8



Dimensions	Value (in mm)
С	1.270
G	0.660
G1	0.820
Х	0.610
X1	4.100
X2	0.755
Х3	4.420
X4	5.610
Y	1.270
Y1	0.600
Y2	1.020
Y3	0.295
Y4	1.825
Y5	3.810
Y6	0.180
Y7	6.610

Site 2:

PowerDI5060-8 (SWP) (Type UX)



Dimensions	Value (in mm)	
С	1.270	
G	0.660	
G1	0.820	
X	0.610	
X1	4.100	
X2	4.420	
Υ	1.270	
Y1	1.020	
Y2	3.810	
Y3	6.610	



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